

Amendments to the Claims:

1. (Cancelled)

2. (Currently Amended) The A bus layout design ~~of claim 1~~ comprising:

a first electrically conductive layer including at least a first bus and a second bus,

a second electrically conductive layer including at least a first bus and a second bus,

an electrically insulating layer disposed between the first electrically conductive layer and
the second electrically conductive layer.

a plurality of vias through said electrically insulating layer conductively connecting said
first electrically conductive layer and said second electrically conductive layer and arranged such
that said first bus and said second bus of said first electrically conductive layer are electrically
connected; and

wherein said first and second electrically conductive layers and said vias are configured
such that said second bus of said first electrically conductive layer is ultimately electrically
connected to said first bus of said second electrically conductive layer yet said vias do not extend
from said second bus of said first electrically conductive layer to said first bus of said second
electrically conductive layer, wherein said plurality of vias connecting said first electrically
conductive layer and said second electrically conductive layer are arranged such that said first
bus and said second bus of said second electrically conductive layer are electrically connected.

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3. (Currently Amended) The bus layout design of ~~claim 1~~ claim 2, wherein said first and second bus of said first electrically conductive layer overlap said second bus of said second electrically conductive layer.

4. (Currently Amended) The bus layout design of ~~claim 1~~ claim 2, wherein said first and second buses of the first electrically conductive layer overlap said second bus of the second electrically conductive layer across the entire input/output width.

5-13. (Cancelled)

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